

# International Workshop for Future Challenges in Tracking and Trigger Concepts

## Preliminary Agenda

| Monday 07.06                            | Tuesday 08.06                   | Wednesday 09.06   | Thursday 10.06   | Friday 11.06  |
|---|---------------------------------|---|--|---|
| <b>Training:</b><br>Vector Classes/SIMD | <b>Training:</b><br>Intel's Ct  | <b>Panel:</b><br>Fixed-Target<br>Experiments<br>(CBM, HADES, PANDA) | <b>Panel:</b><br>Reconstruction Methods<br>(Finding/Fitting) | <b>Panel:</b><br>Software Architectures<br>(Framework/Standalone) |
| <b>Training:</b><br>Multi-Threading     | <b>Training:</b><br>CUDA/OpenCL | <b>Panel:</b><br>Collider Experiments<br>(ALICE, STAR)              | <b>Panel:</b><br>Computer Architectures<br>(CPU/GPU)         | <b>Panel:</b><br>General Discussion<br>Future Plans               |